

IC CARD

Publication number: JP1219982 (A)

Publication date: 1989-09-01

Inventor(s): SHINAGAWA TORU +

Applicant(s): HITACHI MAXELL +

Classification:

- International: B42D15/10; G06F12/14; G06K19/073; G07F7/10; B42D15/10; G06F12/14; G06K19/073; G07F7/10; (IPC1-7): B42D15/02; G06K19/00

- European: G06F12/14C1B; G07F7/10D

Application number: JP19880046304 19880229

Priority number(s): JP19880046304 19880229

Also published as:

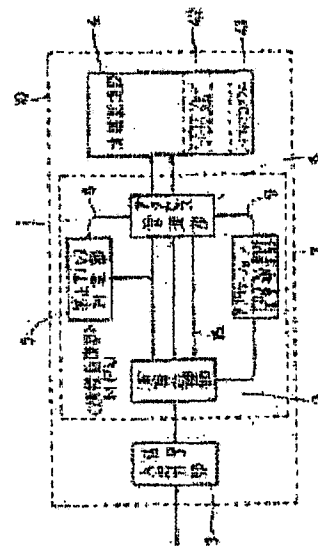
EP0331407 (A2)

EP0331407 (A3)

Abstract of JP 1219982 (A)

PURPOSE: To prevent system control information from being brought to breakdown even if an application program runs away by inhibiting an access of a processor so that an inhibited address is not brought to access by the application program.

CONSTITUTION: The title IC card is provided with an application program operating signal generating part 7 for generating a signal for showing that an application program 42 is operating, and an inhibited address deciding part 5 for holding, for instance, an address for inhibiting an access or an address for permitting an access and generating an access inhibiting signal in case when the inhibited address has been brought to access. From the application program operating signal and the access inhibiting signal, an access managing part 6 inhibits an access to an information storage part 4 by an MPU 1 so that the inhibited address is not brought to access by a start of the application program 42 or its execution.



Data supplied from the *espacenet* database — Worldwide